

ISSUE CLASSIFICATION	
Class	Subclass

RB	O.I.P.E.	PATENT DATE
SCANNED <i>11-11</i>	<i>A</i> Q.A.	

APPLICATION NO. 09/587052	CONT/PRIOR F	CLASS 712	SUBCLASS 24	ART UNIT 2782 2183	EXAMINER Chan, Eddie
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Reconfigurable vliw processor

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div>		NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			ISSUE FEE	
			<div>Amount Due</div>	<div>Date Paid</div>
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			ISSUE BATCH NUMBER	
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